## Crumb128-CAN

## Rapid Prototyping Module with AT90CAN128 µController.

Crumb128-CAN is a low-cost, easy to use and small-formfactor module combining Atmels AT90CAN128 AVR microcontroller with integrated CAN controller with a standard serial port with RS232 transceiver, USB2.0 (full speed) device interface, reset protection circuitry, reset jumper, status LED, standard 6-pin InSystemProgramming (ISP) header and all AT90CAN128 signals on two 32pin headers.



**Connector Kit** – Crumb128-CAN is being shipped without pin headers

mounted, since everybody has it's own favorates (pins male/female facing up/down or 90° angled, etc.). A set of high quality pin headers and receptacles are available as a Crumb128-CAN Connector Kit (see picture).

**Power Supply** – Crumb128-CAN requires a regulated +5V DC power supply. See pinout diagram for VCC pins (both VCC pins are internally connected). The AVCC pin is connected to VCC through coil L1 and decoupled by capacitor C5, hence it should not be connected externally.

**System Reset** – Reset circuitry C11, R7 and D3 provides a proper reset signal after power up and provide protection against irradiation. By closing jumper J1 a manual reset can be triggered.

**System Clock** – The CPU clock is generated by a 16.0000MHz crystal (X1) for error free common serial baudrates. An additional 32.768kHz asynchronous timer crystal can be used for asoftware realtime clock.

**CAN Controller** – The AT90CAN128 provides an integrated CAN controller 2.0A and 2.0B with 15 full message objects with separate identifier tags and masks, transmit, receive, automatic reply and frame buffer receive modes, 1Mbits/s maximum transfer rate at 8 MHz and time stamping, TTC & listening mode (psying or autobaud). The CAN interface signals are available at pins 14 and 15 of the modules headers, see section Connectors below. A sample schematic how to connect a PCA82C250 CAN transceiver is available on the homepage.

**Serial Port** – RS232 level signals are provided by the onboard MAX3221 (U2) RS232 transceiver, connected to UART0 of the AT90CAN128. U2 can be enabled/disabled by jumper J2, which should be set to 1-2 (disabled) or 2-3 (enabled) position before operation. The RS232 signals are available at CON4, which can be connected 1:1 to a male Sub-D-9 connector (see picture).



**USB Port** – The USB interface of Crumb128-CAN is based on Silicon Laboratories CP2102 USB-UART converter, which translates converts the USB data flow into a serial UART format. The CP2102 data sheet, as well as royality-free drivers for Windows, MacOSX and Linux are available on http://www.chip45.com. Please install the USB driver prior to connecting the module to the PC.

Furthermore the CP2102 offers an integrated EEPROM for storing USB parameters like VID, PID, product string and serial number, hence providing the possibility to individualize a product based on Crumb128-CAN, by customizing these parameters. Silabs offers a free Windows tool, as well as another tool for generating a customized Windows USB driver. By providing such a driver to your customers, your product will be recognized immediately when connected to a PC and will be registered in Windows control panel with it's unique product name. Later your product will be assigned the same COM port number each time it is conneced to the PC, which simplifies access to your product from PC applications.

The tools and the corresponding application notes (AN144 and AN220) by Silabs are also available on http://www.chip45.com.

**Status LED** – LED1 is hardwired to pin 17 (OC2 OC1C PB7) of the AT90CAN128 and can be used by the application as a general purpose status indicator. The bootloader flashes the LED three times after power up.

**In-System Programming (ISP)** – CON3 is the standard 6-pin Atmel AVR ISP connector and can be used with most ISP adapters (e.g. CrispAVR-200 or CrispAVR-USB by chip45.com or Atmel's stk500) and software (e.g. uisp, avrdude, PonyProg, etc.). Keep in mind, that the AT90CAN128 shares the ISP signals PDI/PDO with UART0, hence serial communication might be blocked as long as the ISP adapter is connected.

**Memories** – The AT90CAN128 provides 128kbytes of onchip, non-volatile Flash memory for program code storage, 4kbytes of onchip application SRAM and additional 4kbytes of onchip non-volatile EEPROM memory.

If your application requires more memory, CrumbMEM provides 64kbytes of external SRAM memory. CrumbMEM is designed as a pincompatible expansion module and can be stacked underneath Crumb128-CAN, hence providing a very space saving solution.

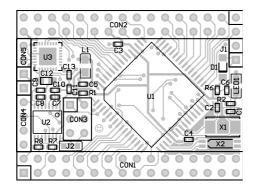


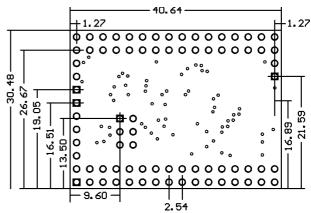
**Fuse Bits** – The AT90CAN128 fuse bits are not preset to any specific values. If you want to use the 16.0000MHz clock crystal instead of the internal oscillator (which is default) set CKSEL[0..3] fuse bits to 0xF.

**Connectors** - All connectors have standard 2.54mm spacing. CON1/2 provide all AT90CAN128 signals, CON3 is the ISP header, CON4 and CON5 provide the RS232 and USB interface signals.

CON1		CON2				CON3	CON <del>1</del>	
	RXDO POI PEO	AVCC	00	GND			MISO 🗖 O VTG	
ΤΧΟΟ ΡΟΟ ΡΕΙ ΟΟ	XCKO AINO PE2	AREF	0 0	PFO	ADCO		SCK <b>O O</b> MOSI	
OC3A AIN1 PE3 OO	OC3B INT4 PE4	ADC1 PF1	0 0	PF2	ADC2		∕RST <mark>O O</mark> GND	<b>о</b>   тхр
0C3C INT5 PE5 0 0	T3 INT6 PE6	ADC3 PF3	0 0	PF <del>1</del>	ADC4	тск		
IC3 INT7 PE7 OO	∕SS PBO	ADC5 TMS PF5	0 0	PF6	ADC6	тоо	T4	O GND
SCK PB1 O O	MOSI PB2	ADC7 TDI PF7	0 0	GND			JI	
MISD PB3 OO	DCO PB <del>1</del>	VCC	0 0	PAO	ADO			CONE
0C1A PB5 0 0	OC1B PB6	AD1 PA1	0 0	PA2	AD2		C /RESET	CON5
DC2 OC1C PB7 O O	NC	AD3 PA3	0 0	PA4	AD4			O D+
	✓RESET	AD5 PA5	0 0	Pa6	AD6		70	<b>0</b>   D-
0 0 DOV	GND	AD7 PA7	0 0	PG2	ALE		J2	D VBUS
NC O O	NC	A15 PC7	0 0	PC6	A1 4		3 🔳 1	
SCL INTO PDO OO	SDA INT1 PD1	A13 PC5	00	PC4	A1 2		2	
RXD1 INT2 PD2 OO	TXD1 INT3 PD3	A11 PC3	0 0	PC2	A10		1-2 => disable	
IC1 PD4 OO	XCK1 PD5	A9 PC1	0 0	PCO	A8		2−3 => enable	
T1 PD6 <b>0 0</b>	T2 PD7	∕RD PG1	00	PGO	∕WR		R5232	

**Board Layout and Dimensions** – Crumb128-CAN is a ~40x30mm<sup>2</sup> double-sided FR4 PCB (1.6mm). Header holes have 0.9mm diameter, hence most standard round or rectangular pin headers can be mounted. See pictures for details.





**Development Tools** – Crumb128-CAN is based on the ATmega128 AVR microcontroller, which can be programmed either in assembler (e.g. the original AVR Studio by Atmel: http://www.atmel.com/avr) or with several high level languages, including C/C++, Pascal or Basic. There exist several commercial C/C++ compiler suites (e.g. IAR Embedded Workbench or CodeVisionAVR) as well as the WinAVR GNU C/C++ compiler and tools suite (see http://winavr.sourceforge.net for details, the bootloader of Crumb128-CAN was developed with WinAVR). A suitable and reasonably priced Basic compiler is BASCOM-AVR by http://www.mcselec.com. For a good Pascal environment please go to http://www.e-lab.de.

**Code Examples** – The Crumb128-CAN download page (www.chip45.com/Crumb128-CAN\_Downloads) provides free code examples for using the CAN controller.

**Further Information** – Application notes and data sheets of the onboard components as well as the schematics can be downloaded at http://www.chip45.com. The official Atmel AVR homepage is http://www.atmel.com/avr. A valuable source of information dedicated to AVR microcontrollers is http://www.avrfreaks.net.

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